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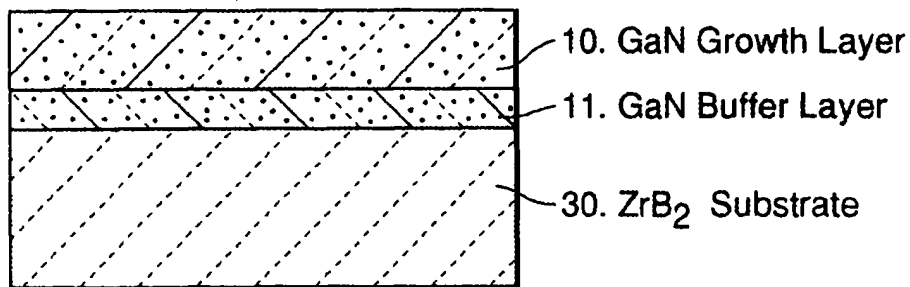
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(54) **A boride-based substrate for growing semiconducting layers thereon and a semiconductor device using the same**

(57) A substrate for forming a semiconducting layer is provided to grow the semiconducting layer on a major surface thereof, wherein the substrate comprises a single crystal of a chemical formula of XB_2 where X contains one of Ti and Zr and the major surface may preferably be substantially parallel to plane (0001) of the single crystal because the plane (0001) of the boride substrate is highly coherent to the lattices of GaN and AlN layers grown epitaxially on the substrate. The single crystal of the substrate may be a solid solution contain-

ing impurities of not more than 5%, wherein at least one of the impurities is one selected from Cr, Hf, V, Ta and Nb. Further, a semiconductor device includes the substrate of a single crystal of a chemical formula of XB_2 and at least one semiconducting layer which is grown epitaxially on the substrate, the semiconducting layer including a nitride semiconductor of a chemical formula of ZN where Z is one of gallium, aluminum and indium and boron. The device can be used for a light emission diode in which one or more connection electrodes are attached on the substrate.

Fig.4



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Description

BACKGROUND OF THE INVENTION

5 Field of the Invention

[0001] The present invention relates to a boride-based substrate for forming semiconducting layers thereon to be used for a semiconductor device, and to a semiconductor device using the boride-based substrate.

10 Prior Art

[0002] Gallium nitride based semiconductors have been used for devices for emitting blue to violet rays such as light emission diodes and laser emitting semiconductor devices, and also have been noticed as electron controlling devices having wider energy gaps than silicon and gallium arsenide semiconductors.

15 [0003] In the light emission diodes as examples of conventional semiconductor devices making use of gallium nitride layers, the layers have been formed on the surface for a substrate made of a sapphire crystal, as described in Japanese Patent Publication No. A 4-321280.

[0004] Fig. 8 shows a prior art structure of a light emission diode, in which, on a sapphire substrate 20 is formed a GaN buffer layer 11 on which a GaN-grown multilayer is laminated including, in turn, an n-GaN layer 16, an n-AlGaN clad layer 15, an InGaN emission layer 14, a p-AlGaN clad layer 12 and a p-GaN layer 12.

20 [0005] The grown multilayer including p-GaN layer 12 to n-GaN layer 16 is partially cut by etching to expose an upper surface of n-GaN layer 16 on which an n-type electrode 41 is attached, where a p-type unipolar electrode 40 is connected on the top surface of p-GaN layer 12 as a uppermost layer. This type of light emission diode is referred to as lateral type.

25 [0006] Since a single perfect crystal for use of substrates is difficult to be made of GaN, some methods of preparing perfect single crystals containing other substance than GaN have been considered to grow GaN semiconducting layers effectively thereon. For example, proposals have been made to use plane (0001) of spinel $\text{MgO} \cdot \text{Al}_2\text{O}_3$ or silicon carbide such as 6H- or 4H-SiC for growing semiconducting layers.

30 [0007] Growth of GaN-based semiconducting layers has usually been achieved by using sapphire crystals. A buffer layer had to be formed previously of AlN or GaN in the amorphous form on the particular plane of the sapphire substrate because no single crystal layer of GaN having few defects can be formed directly on the sapphire crystal due to a considerably large lattice constant differential between an Al_2O_3 crystal and a GaN crystal.

35 [0008] The lattice of a substrate crystal must match with a GaN or AlN layer to be epitaxially grown thereon to form a perfectly single crystal layer. For the lattice constants of GaN and Al_2O_3 , a reference length, on plane (0001), of sapphire corresponding to the lattice constant of GaN or AlN is 1/1.73 times as long as an a-axis lattice length of a unit lattice cell of sapphire because the GaN or AlN crystal is grown rotating its a-axis by 30° to the a-axis of the sapphire unit cell on its plane (0001). The lattice constant a of sapphire is $a=4.7580 \text{ \AA}$, then the reference length of sapphire being estimated 2.747 \AA . On the contrary, GaN and AlN have a lattice constant of 3.168 \AA and 3.186 \AA , respectively, then having a percentage of lattice mismatch rate of +15.98% and +13.27%, respectively, which is a ratio of the differential of the nitrides lattice constant from the reference length divided the reference length. The lattice constant of sapphire is excessively larger than that of GaN and AlN from point of view of lattice coherency required for epitaxial growth. This is a reason for preparing a buffer layer on the plane (0001), i.e., the major surface of the substrate, prior to forming GaN crystal layers. Such a buffer layer serves to relax the lattice mismatch between the GaN or AlN layer and the substrate.

45 [0009] However, the GaN grown layers formed on the buffer layer have often included numerous lattice defects, particularly dislocations, which often reach a larger lattice defect density in a range of $10^7 - 10^{11} \text{ cm}^{-1}$, compared to $10^2 - 10^7 \text{ cm}^{-1}$ of the defect density in some GaAs-based semiconducting layer on a GaAs-based semiconducting substrate.

50 [0010] The presence of numerous lattice defects in the GaN grown layers has limited property performance of the produced semiconductor devices and the GaN layers have been required to increase in impurities to be contained therein to emit more carriers. However, this results in further increase in lattice defects created in the semiconducting layers, then deteriorating light emission semiconductor device in all quality performance including parameters such as useful lifetime, withstand voltage, driving voltage, consumable power or power efficiency, operating speed and current leakage.

55 [0011] There was another problem that no electrode was provided for any surface of a sapphire substrate to connect the semiconducting layers in the device to an outer circuit because sapphire is an electrically insulating material having no conductivity across the substrate. By this reason, it was required to provide a unipolar electrode on any conductive or semiconducting layer over a substrate, resulting in limited disposition of electrodes in the device. In the case shown

in Fig. 8, an n-type unipolar electrode 41 for connection is attached on a surface of a lower semiconducting layer, e. g., n-GaN layer 16, and accordingly n-GaN layer 16 must previously be provided with a surface by removing partially the layers including p-GaN layer 12 to the n-GaN layer 16 by an etching process. However, thickness of the n-GaN layer 16 whose surface is used for attaching the electrode 41 is not easily controlled to be uniform in the etching process, leading to complicated production processes and low product yield.

[0012] A sapphire substrate does not efficiently radiate out heat created in the semiconductor device due to its low thermal conductivity, which causes the device to increase in operating temperature, then to decrease in useful life time. This problem was required to be solved by particular measures for promoting heat radiation from the substrate.

[0013] There was another problem due to a thermal expansion differential between a sapphire substrate and a GaN- or AlN-buffer layer, having rather large difference in thermal compression coefficient. In general, the GaN layer gets grown on the surface of a sapphire substrate at a temperature higher than 1000°C. During cooling the layer together with the substrate from such a high growth temperature to room temperature, the substrate is contracted so as to form a compression stress in the grown GaN-layer due to a significant difference in thermal compression coefficient between the layer and the substrate, causing residual stress in the GaN layer to increase the density of dislocations and other crystal defects.

SUMMARY OF THE INVENTION

[0014] An object of the present invention is to provide a substrate for growing directly a semiconducting nitride layer thereon with sufficiently low density of lattice defects in the layer, by approaching the lattice constant of the substrate to that of the nitride layer.

[0015] Another object of the present invention is to provide a substrate for growing a semiconducting nitride layer, which substrate has an electric conductivity high enough to form a connecting electrode directly on the front or rear face of the substrate.

[0016] Further another object of the present invention is to provide a substrate having high thermal conductivity to effectively radiate heat produced in a semiconductor device formed on the substrate.

[0017] Still another object of the present invention is to provide a substrate for growing a semiconducting layer wherein the substrate has a thermal expansion coefficient less different from that of the semiconducting nitride layer to be formed to prevent a residual stress from creating between the substrate and the layer.

[0018] Further still another object of the present invention is to provide a semiconductor device using such a substrate which can provide excellent quality performances of high heat radiation and formation of electrodes on the substrate.

[0019] In the present invention, a substrate for growing a semiconducting nitride layer comprises a single crystal of a boride having chemical formula XB_2 where X is Ti or Zr. The crystal of the boride XB_2 has a principal plane (0001), expressed by hexagonal indices, which can be used for a major surface of a substrate on which to grow the layers of a nitride, and has a lattice constant highly coherent in the plane (0001) to that of the nitride, particularly, of gallium Ga, aluminum Al, indium In or boron B.

[0020] The present invention includes a semiconductor device comprising the above-mentioned substrate of a boride having chemical formula XB_2 where X is Ti or Z and at least one semiconducting layer formed on the substrate, wherein the semiconducting layer includes a nitride semiconductor of a chemical formula of ZN where Z is one of gallium, aluminum and indium and boron.

[0021] On the boride XB_2 substrate a single crystal layer of the nitride semiconductor can easily be grown epitaxially in very low level of lattice defect density. The plane (0001) of the XB_2 substrate has high coherency with respect to the lattice of the single crystalline nitride layer at the interface of both crystals. This reduces the lattice strain in the nitride layer, and then, reduces the amount of dislocations to be induced during growth.

[0022] In the present invention, an epitaxially grown nitride layer may be formed directly (i.e., without using any buffer layer) from the substrate surface, and further semiconducting nitride layers may be laminated in turns on the first nitride layer by epitaxial growth steps on the directly grown nitride layer. The direct growth of a semiconducting nitride layer on the substrate can provide advantages of high crystallinity and very low density of lattice defects in the laminated semiconducting nitride layers to improve the above various properties needed for semiconductor devices.

[0023] The boride including Ti or Zr in the present invention has a high electric conductivity. It is advantageous that the boride substrate can be used to attach a connecting electrode directly thereon when it is used as a substrate for fabricating a semiconductor device.

[0024] The boride including Ti or Zr in the present invention has a relatively high thermal conductivity, then providing another advantage of enhancing the heat radiation created in the semiconductor device from the substrate effectively.

[0025] The boride including Ti or Zr has a thermal expansion coefficient close enough to that of the nitrides of Ga or Al to reduce occurrence of residual thermal stress between a boride substrate and a nitride layer, which considerably avoids introducing dislocations into the semiconducting nitride layers during the thermal process, and provides thermally stable performance for the semiconductor device for a long time.

BRIEF DESCRIPTION OF THE DRAWINGS

[0026] The present invention will be explained in further detail below with reference to the accompanying drawings, in which:

- 5 Fig. 1A shows an atomic configuration (AlB₂ type structure) in a unit cell of a XB₂ compound crystal;
 Fig. 1B shows a top view of the unit cell of a ZrB₂ crystal viewed perpendicular to a rhombic plane of the unit cell shown in Fig. 1A;
 Fig. 2A shows an atomic configuration (wurtzite type structure) of a unit cell of a GaN crystal;
 10 Fig. 2B shows a top view of a GaN crystal viewed perpendicular to a rhombic plane of the unit cell shown in Fig. 2A;
 Fig. 3 shows a Laue diagram of X rays diffracted by plane (0001) of a ZrB₂ crystal;
 Fig. 4 shows a longitudinally cross-sectional view of the arrangement of a GaN growth layer on a ZrB₂ substrate;
 Fig. 5 shows a X-ray diffraction chart of GaN grown layer on a ZrB₂ substrate where the lateral axis shows a diffraction angle and the longitudinal axis shows a diffracted X-ray intensity;
 15 Fig. 6 shows a spectrum chart of deflected ray on the surface of a GaN growth layer on a ZrB₂ substrate where the lateral axis shows a wavelength of light and longitudinal axis is a light reflectance of the surface of the GaN grown layer;
 Fig. 7 shows a cross-sectional view of a GaN based light emission diode fabricated on a ZrB₂ substrate according to the embodiment of the present invention; and
 20 Fig. 8 shows a cross-sectional view of a prior art GaN based light emission diode fabricated on a sapphire substrate.

THE DETAILED DESCRIPTION OF THE INVENTION

25 [0027] The substrate of the present invention includes a single crystal of a boride expressed by XB₂ where X is Ti or Zr, and B is boron. XB₂ may include TiB₂, ZrB₂ or their solid solution (Ti, Zr)B₂. The crystal structure of these XB₂ compounds have a hexagonal structure referred to as AlB₂ structure shown in Fig. 1A, which is similar to wurtzite structure of a GaN crystal shown in Fig. 2A.

30 [0028] X in the formula XB₂ may contain small amounts of impurities, capable of dissolving in the XB₂ crystal, such as Hf, V, Nb, Ta, Cr. The total amounts of the impurities preferably are not more than 5 mol% based on total X components. The impurities of an amount exceeding 5 mol% have a risk of varying the excellent characteristics of the XB₂ material as will be explained later.

35 [0029] The substrate of the present invention can use crystal plans (0001), (01 $\bar{1}$ 0), (1 $\bar{1}$ 20) and (01 $\bar{1}$ 2), expressed in the hexagonal indices, of the XB₂ crystal containing Ti or Zr for a major surface on which to grow the semiconducting nitride layer of GaN, AlN, InN and BN. Particularly, plane (0001) is desirable for growth of the nitride layer with small lattice defects. The major surface of the substrate polished parallel to plan (0001) may preferably be used for the formation of the growth layers of GaN and AlN crystals or mixed crystal thereof because of the high coherency of the plane (0001) to the lattice of GaN and AlN crystals.

40 [0030] As will be explained in a methods of the later Example, the lattice constants of XB₂ crystals were determined by using samples produced of TiB₂ and ZrB₂. Table 1 lists the newly determined lattice constants of borides ZrB₂ and TiB₂, with together the data of alumina Al₂O₃, and nitride compounds GaN and AlN. A lattice constant is defined as a length of a side of the rhombic plane on a sectional-rhomboid cylinder, a unit cell, of the borides and the nitrides, as shown in Figs 1B and 2B, respectively, wherein the rhombic plane corresponding to plane (0001).

Table 1

	Lattice Constant Å
ZrB ₂	3.1696
TiB ₂	3.0303
Al ₂ O ₃	a1=4.7580 a2=2.747
GaN	3.186
AlN	3.1114

55 [0031] Table 2 shows data of lattice coherency of one of GaN and AlN to one of TiB₂, ZrB₂ and alumina. It is from this Table found that TiB₂ and ZrB₂ exhibit higher coherency to the Nitrides of Ga and Al than sapphire does. Particularly, ZrB₂ is very highly coherent to either of GaN and AlN within a range of 2% of the differential of the lattice constants between the boride and the nitride.

Table 2

	Al ₂ O ₃	ZrB ₂	TiB ₂
lattice incoherency rate to GaN (%)	15.98	-0.51	-4.89
lattice incoherency rate to AlN (%)	13.26	1.87	-2.61
thermal conductivity (Cal/cm·sec·°C)	0.10	0.32	0.18
thermal expansion differential to GaN (%)	3.2	2.7	1.1
specific resistance (μΩ·cm)	>10 ¹⁰	17	9

[0032] The semiconductor device of the present invention includes a substrate formed of TiB₂, ZrB₂, or their solid solution, and at least one semiconducting layer formed of nitride of at least one of gallium, aluminum, indium and boron which is epitaxially grown on the substrate. The semiconducting nitride layers have high coherency to Plane (0001) on the surface of the boride substrate, and accordingly, can have high crystallinity with very low density of lattice defects such as dislocations.

[0033] As shown in Table 2, the substrate of the present invention shows the feature that the boride XB₂ crystal has the thermal expansion coefficient close to that of the nitride of gallium and aluminum, with a smaller differential of the thermal expansion coefficients between the boride XB₂ substrate and the nitride layer, compared to the conventional sapphire substrate. Therefore, during cooling process after the GaN layer growth, a strain applied to the nitride layer in the direction along plane (0001) may be kept much lower, compared to the use of a sapphire substrate. This reduces a warp or deformation created by temperature change in the substrate having a layer grown thereon, and effectively lowers an amount of dislocations or other defects in the nitride layer, which otherwise would be induced by large deformation.

[0034] Since, as shown in Table 2, the borides XB₂ show relatively high electrical conductivity enough to provide a substrate with an connecting electrode to electrically connect the nitride layers on the substrate to an outer lead. The electrode may be attached to the substrate on not only its top major surface for forming the layers thereon, but also the opposite surface to the top major surface. Particularly, the formation of the electrode to the rear major surface is effective to make the device more compact because there is no need to etch and cut the semiconducting nitride layers in order to form a particular place on which an electrode should be applied. Further, the substrate in the present invention may be used as an connecting electrode, i.e., lead wires may directly be connected to some area of a surface of the substrate.

Embodiment 1.

[0035] For producing a boride crystal including Ti and/or Zr, known processes of producing a single crystal can be used, such as the flux melting process using an aluminum flux, the zone melting process by lamp focusing heating, the induction zone melting, and especially, the induction zone melting is preferable because of electric conductivity of the borides. Although these borides have very high melting temperatures of 3050°C ± 5°C for ZrB₂ and 2790°C ± 5°C for TiB₂, the borides are electrically conductive enough to apply induction heating to melt the nitrides.

[0036] Comfortably, these borides are easily crystallized into a large crystal grain during solidification from its molten state. In the process of the floating zone melting of a elongated material of XB₂, a locally heated and melted part of the material is moved slowly along the longitudinal direction of the material, and then the molten part, or zone, is cooled gradually to crystallize. During cooling the molten zone, a particular crystal grain among many grains created at the beginning of solidifying can crystallize fast and preferentially into a large single crystal.

[0037] A nitride single crystal so produced is cut out into blocks such that the cut surface of the blocks should be a desired crystal plane such as plane (0001). Then, in order to provide the plates as substrates for growing nitride layers, the crystal plane on the plate surface is determined precisely, for example, by using an X-ray diffraction technique. Actually, cutting of the surfaces and determining of the crystal orientations are repeated until the cut surface of the single crystal is precisely matched to plane (0001) of the crystal, while observing diffracting patterns from the X-ray diffraction method.

[0038] The boride crystal is ground and polished with diamond abrasive because of high hardness of the crystal. The method of grinding is preferable a wet grinding process using a diamond abrasive wheel having a grain size range of 400 mesh to 1000 mesh according to JIS. The grain size rougher than 400 mesh is not desirable because the rough grained abrasive increases a risk of producing cracks inside the single crystal. On the other hand, the finer grain size than 1000 mesh is not practical because of the lower grinding speed. Other abrasives than diamond is undesirable because the high efficiency of grinding can not be obtained only to make the cut surfaces smooth like a mirror.

[0039] In the present invention, in further detail, the cut surface of the boride crystal is measured by the θ -2 θ method of the X-ray diffraction technique and polished by the abrasive, the measuring and polishing being repeated to meet the cut surface precisely with a particular crystallographic plane such as plane (0001). The finishing processes include determining a diffraction angle of X ray diffracted from a cut surface of the boride crystal, and calculating an angle and orientation of the cut surface with respect to the nearest plane (0001), for example, of the crystal. Further, the processes include recutting the cut surface according to the determined angle and orientation so as to approach the plane orientation of the cut surface to the plan (0001) as precisely as possible. These processes of measuring X-ray diffraction from the cut surface of the crystal and recutting the cut surface are in return repeated until a Laue backside pattern of Plane (0001) is clearest on diffracting a X-ray from the recut surface of the crystal. Fig. 3 shows a Laue backside pattern of a X ray diffracted from plane (0001) of a ZrB_2 single crystal.

[0040] Thus, the single crystal having the surface precisely determined to have plane (0001) is sliced into a number of thin substrates each having a surface parallel to plane (0001). The step of slicing of substrates may use a bandsaw.

[0041] The substrates are polished on the determined surface and then finished into a mirror surface. To polish the substrates, diamond abrasive may be used and the abrasive preferably may have a grain size under 3 μm , the grain size over 3 μm giving risk of surface defect to the substrate.

[0042] In the finishing step for the mirror surface, the substrates may be chemically polished in the solution of potassium hydroxide in water. It is preferable that, to smooth the surface parallel to plane (0001), the potassic solution be set to a temperature of 30°C or less and to the range of pH 8.0 to 8.5. If the solution outside is outside said pH range, selective etching would occur on the surface of plane (0001), producing numerous projections, each less than 1 μm high and 3 - 20 μm in diameter, on the surface. In the case of the surface having other planes than plane (0001), the chemical polishing technique with a larger range of pH may avoid so selective, defective etching, obtaining a very smooth major surface on the substrate.

[0043] The single crystal substrates of ZrB_2 are washed with organic solvent, such as acetone or methanol, and rinsed with water, preferably, at a temperature of 30 °C or less to avoid formation of an oxide layer coating on the surface of the substrate.

(Example 1.)

[0044] Samples of ZrB_2 and TiB_2 were prepared and tested in the followings, to determine crystallographic and thermal properties of those borides.

[0045] The samples of bar-like single crystals ZrB_2 and TiB_2 were prepared by a floating zone melting process to a dimension of 10 mm in diameter and 60 mm in length, and, after determining Plane (0001) for each sample, they were used to measure thermal expansion coefficient and thermal conductivity in a direction perpendicular to axis <0001>.

[0046] A specimen was cut out of each of the samples into a bar having a length of 9 to 16 mm along in a direction perpendicular to axis <0001>. In measuring thermal expansion, the specimen was placed in a thermal analyzer (type TAS-200 manufactured by RIGAKU Co. Ltd.).

[0047] In measuring thermal conductivity, the specimen prepared in the same way was used by applying a laser-flash method (using the apparatus of type TC-7000 by SHINKU-RIKO Co. Ltd.).

[0048] A lattice constant of each sample was determined by X-ray diffraction method (using the diffractometer of type PHILIPS PW3050).

[0049] Table 3 shows the resulting data of thermal expansion coefficients, thermal conductivities and lattice constants of single crystals of ZrB_2 and TiB_2 , and, for comparison, lists the published data of polycrystalline borides by " Illustrated Crystallochemistry of Fine Ceramics", Agne Technical Center Co., Tokyo. From the data in Table 3, it is found that the thermal data of ZrB_2 and TiB_2 are significantly different between a single crystal and a polycrystal.

Table 3

	ZrB_2		TiB_2	
	measured single-crystal	poly-crystal	measured single-crystal	poly-crystal
expansion coefficient $10^{-6}/^{\circ}C$	8.3058	6.8	6.758	5.5
thermal conductivity (cal/cm-sec- $^{\circ}C$)	0.32	0.058	0.18	0.01
lattice constant (\AA)	3.1696	3.1670	3.0303	3.0245

Embodiment 2.

[0050] The semiconductor device of the present invention includes the substrate of a single crystal of XB_2 , wherein X is Ti or Zr or their solid solution and the substrate has a major surface parallel to a particularly crystallographic plane, such as plane (0001), and at least one or more semiconducting layers of nitride which is epitaxially grown on the major surface of the substrate. The semiconducting layer is constituted by a nitride containing one or more elements of Ga, Al, In and B. Also, a plurality of semiconducting layers of such a nitride may be formed by laminating another semiconducting layer over other on the major surface of the substrate. In the semiconductor device of the present invention, particularly, a GaN-based grown layer may be adapted to the surface of plane (0001) of the XB_2 -based substrate because of high lattice coherency of the substrate to the above GaN-based grown layer. The following shows a method of producing a semiconductor device using GaN-based grown multilayers on the substrate of ZrB_2 .

[0051] The method of growing a semiconducting layer can use any of the techniques of molecular beam epitaxy (MBE), metal organic vapor phase epitaxy (MOVPE), hydride vapor phase epitaxy (HVPE), and sublimation, and a combination of those techniques. For example, in an earlier step of forming each layer, the MBE technique may be used for growing a layer while controlling the surface condition of the layer and in the later step, the HVPE may be used for growing the layer thicker because of high speed growth.

[0052] In the present invention, the first layer such as GaN layer may be formed directly on the substrate without any buffer layer. However, as shown in Fig. 4, a buffer layer 11 may be formed on the major surface of the substrate 30, on which the layer 11 is grown. Such a buffer layer may be composed of GaN, ZnO, BN, SiC or the like.

[0053] The semiconductor device of the present invention is wisely applicable to devices, using one or more semiconducting layers of GaN or other semiconducting nitrides, for example, a light emission device, a laser diode, a transistor, a triac, a thyristor. The semiconductor devices are effectively capable of exhibiting properties of good conductivity and high heat radiation of the substrate and high lattice coherency between the growth layer and the substrate.

25 (Example 2)

[0054] The example of the growing of the GaN layer on the surface of plane (0001) of a ZrB_2 substrate is shown as follows. The single crystal of ZrB_2 was prepared by floating zone melting and sliced into substrate having a major surface of plane (0001). A GaN layer was grown on the major surface of the substrate by using MBE technique under a vacuum where a Ga molecular beam and activated nitrogen in a high frequency excited plasma cell were provided onto the surface of the ZrB_2 substrate heated up to 800 °C, to grow a layer thereon. The Ga beam was supplied at a pressure of 10^{-2} Pa with a nitrogen flow of 0.5 ccm/min and power supply of 400 W.

[0055] Fig. 5 shows a chart of X-ray diffraction to the structure of the GaN-layered ZrB_2 substrate. As the diffraction of plane (0001) of both layer and substrate was measured through the θ -2 θ method, and the X-ray peaks only from planes (0002) and (0004) were observed, indicating that the GaN layer was grown epitaxially from the ZrB_2 substrate.

[0056] Fig. 6 shows a spectrum distribution of surface reflectance from the GaN-layered ZrB_2 substrate, indicating definite periodic modulation of the reflectance spectrum due to critical optical interference between the GaN layer and the ZrB_2 substrate, this interference behavior reflecting the precise flatness of the layer and the smoothness of the interface therebetween. As shown in Fig. 6, the wavelength at which the periodic modulation diminishes is 368.5 nm, corresponding to an optical band gap at room temperature, which exhibits high optical property of the GaN layer epitaxially formed on the ZrB_2 substrate.

Embodiment 3.

[0057] Fig. 7 shows a vertically cross-sectional view of a light emission diode having a GaN-based double-hetero p-n multilayer structure on a ZrB_2 substrate. In this light emission diode, the GaN-based multilayer includes, on a GaN buffer layer 11 on the substrate 30, a Si-doped n-GaN layer 16, a Si-doped n-AlN clad layer 15, a Si- and Zn-doped n-InGaN light emission layer 14, a Mg-doped p-AlGaIn clad layer 13 and a Mg-doped p-GaN layer 12, in this order. In this diode, because the substrate itself, electrically conductive, is used for an electrode for electrical connecting the multilayer to an outer lead (not shown).

[0058] Thus, the use of a ZrB_2 substrate for a GaN-based semiconductor device shows the following advantages. The electrode structure in the device can be simplified by using the substrate as an electrode because of its conductivity. The electric and optical performance of the semiconductor device can be improved because of GaN growth layers each having high coherency to the ZrB_2 substrate and having high crystallinity with a very low level of lattice defects. Further, the light emission device can be high in light emission efficiency, i.e., the light from a light emitting layer can be reflected to the opposite side to the substrate because of metallic gloss with no transmittency of the ZrB_2 and TiB_2 crystal substrates.

[0059] Furthermore, the heat radiation property of the semiconductor device can be improved by high thermal con-

ductivity of the ZrB_2 and TiB_2 crystal substrates.

Claims

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1. A substrate for forming a semiconducting layer to grow the semiconducting layer on a major surface thereof, wherein the substrate comprises a single crystal of a chemical formula of XB_2 where X contains at least one of Ti and Zr.

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2. The substrate according to Claim 1, wherein the major surface is substantially parallel to plane (0001) of the single crystal.

3. The substrate according to Claim 1 or 2, wherein the single crystal is a solid solution containing impurities of not more than 5%.

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4. The substrate according to Claim 3, wherein at least one of the impurities is one selected from Cr, Hf, V, Ta and Nb.

5. The semiconductor device comprising the substrate according to any of claims 1 to 4 and a semiconducting layer which is grown epitaxially on the substrate.

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6. The semiconductor device according to claim 5, wherein the semiconducting layer comprises a nitride semiconductor of a chemical formula of ZN where Z is at least one of gallium, aluminum and indium and boron.

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7. The semiconductor device according to claim 5 or 6, wherein the semiconducting layer is formed on a buffer layer which is formed on the substrate.

8. The semiconductor device according to Claim 7, wherein the buffer layer is an amorphous or crystalline phase of a nitride containing at least one of gallium and aluminum.

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9. The semiconductor device according to any of claims 5 to 8, wherein one or more connection electrodes are attached on the substrate.

10. The semiconductor device according to any of claims 5 to 9, wherein the semiconductor device is a light emission diode including a light emission layer based on gallium nitride therein.

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Fig. 1A

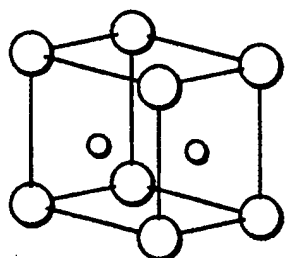


Fig. 1B

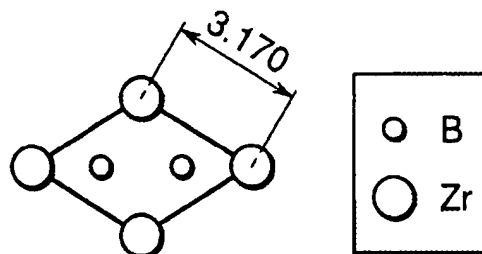


Fig. 2A

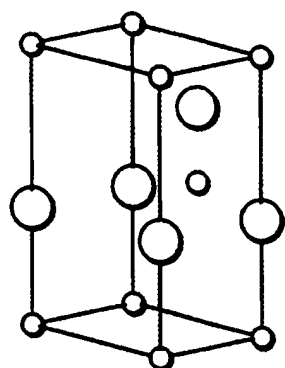


Fig. 2B

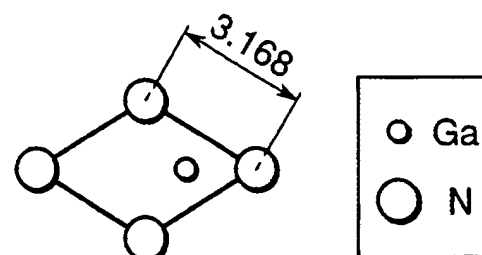


Fig.3

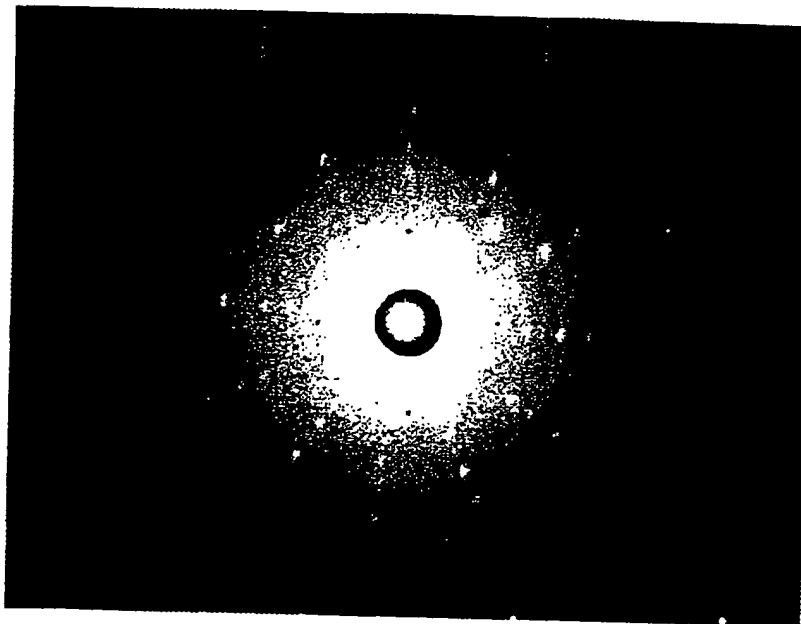


Fig.4

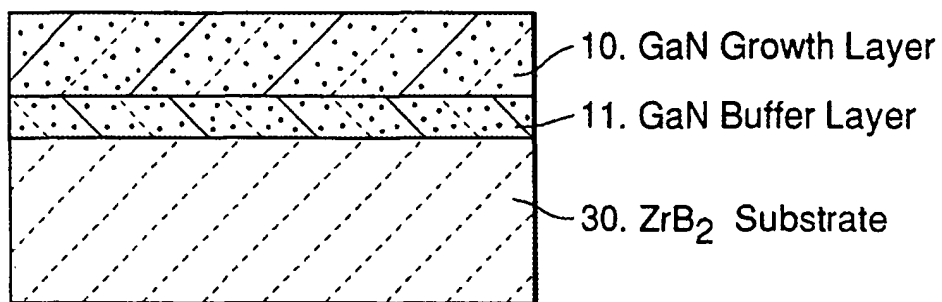


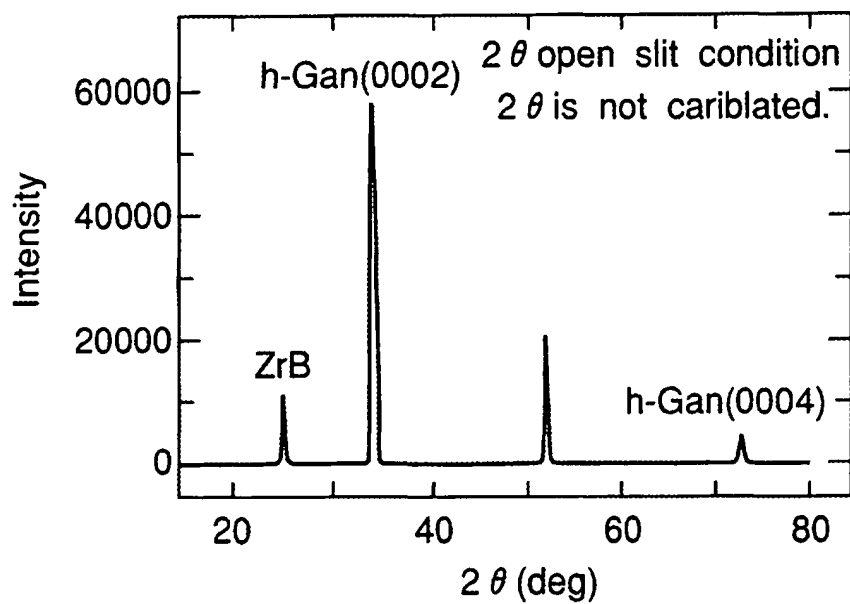
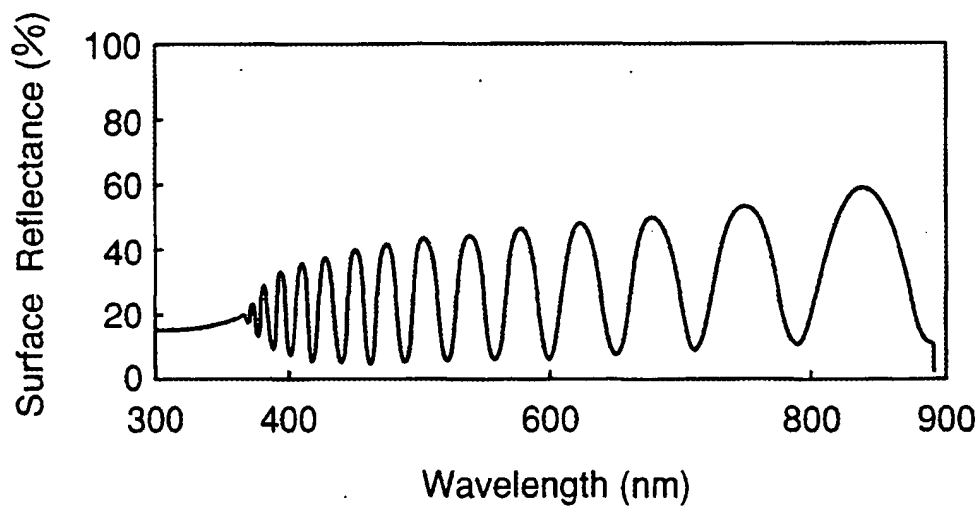
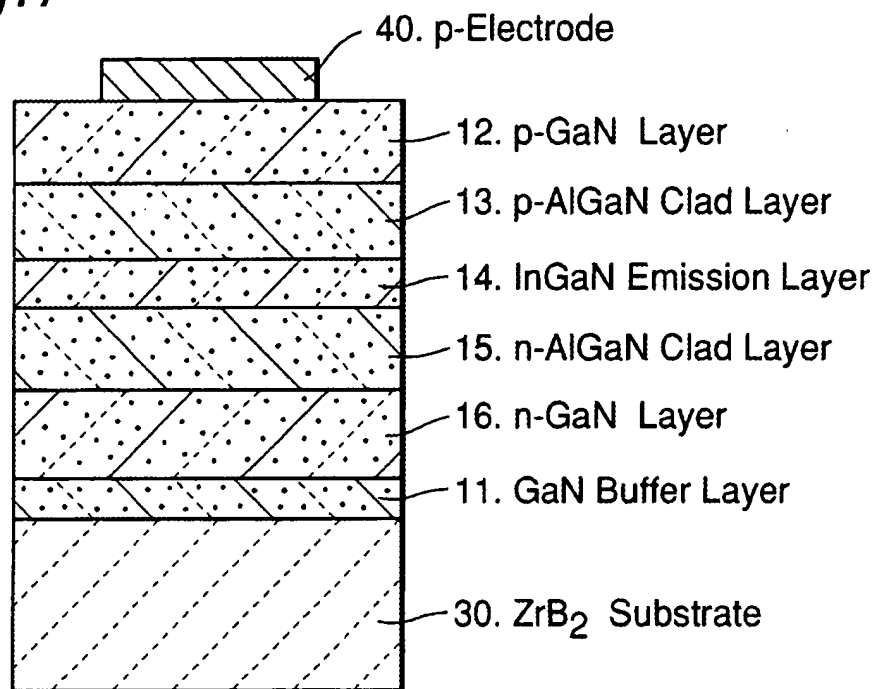
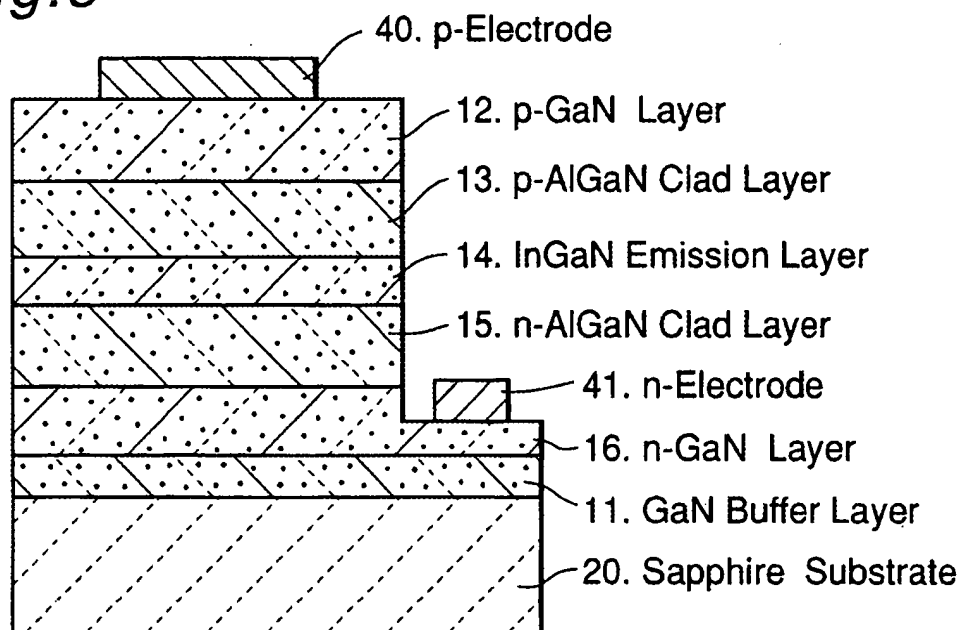
Fig.5*Fig.6*

Fig.7*Fig.8*

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